

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Atty Dkt. 1035-473

C# M#

KIMURA et al

TC/A.U.2811

Serial No. 10/668,166

Examiner: Vu, Quang D.

Filed: September 24, 2003

Date: August 5, 2004

Title: SEMICONDUCTOR DEVICE AND CHIP-STACK SEMICONDUCTOR DEVICE



Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

RESPONSE/AMENDMENT/LETTER

This is a response/amendment/letter in the above-identified application and includes an attachment which is hereby incorporated by reference and the signature below serves as the signature to the attachment in the absence of any other signature thereon.

☐ **Correspondence Address Indication Form Attached.****Fees are attached as calculated below:**

Total effective claims after amendment 20 minus highest number
previously paid for 20 (at least 20) = 0 x \$ 18.00 \$ 0.00

Independent claims after amendment 4 minus highest number
previously paid for 5 (at least 3) = 0 x \$ 86.00 \$ 0.00

If proper multiple dependent claims now added for first time, add \$290.00 (ignore improper) \$ 0.00

Petition is hereby made to extend the current due date so as to cover the filing date of this
paper and attachment(s) (\$110.00/1 month; \$420.00/2 months; \$950.00/3 months) \$ 0.00

Terminal disclaimer enclosed, add \$ 110.00 \$ 0.00

☐ First/second submission after Final Rejection pursuant to 37 CFR 1.129(a) (\$770.00) \$ 0.00

☐ Please enter the previously unentered, filed

☐ Submission attached

Subtotal \$ 0.00

If "small entity," then enter half (1/2) of subtotal and subtract -\$ 0.00

☐ Applicant claims "small entity" status. ☐ Statement filed herewith

Rule 56 Information Disclosure Statement Filing Fee (\$180.00) \$ 0.00

Assignment Recording Fee (\$40.00) \$ 0.00

Other: **THREE SHEETS OF DRAWINGS OF FIGS. 15-17** 0.00

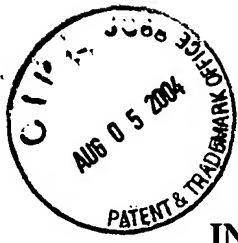
TOTAL FEE ENCLOSED \$ 0.00

The Commissioner is hereby authorized to charge any deficiency, or credit any overpayment, in the fee(s) filed, or asserted to be filed, or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Account No. 14-1140. A duplicate copy of this sheet is attached.

1100 North Glebe Road, 8th Floor
Arlington, Virginia 22201-4714
Telephone: (703) 816-4000
Facsimile: (703) 816-4100
JAR:ap

NIXON & VANDERHYE P.C.
By Atty: Joseph A. Rhoads, Reg. No. 37,515

Signature: 



ITW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

KIMURA et al

Atty. Ref.: 1035-473; Confirmation No. 4031

Appl. No. 10/668,166

TC/A.U. 2811

Filed: September 24, 2003

Examiner: Vu, Quang D.

For: SEMICONDUCTOR DEVICE AND CHIP-STACK SEMICONDUCTOR DEVICE

* * * * *

August 5, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

AMENDMENT

Responsive to the Official Action dated May 6, 2004, please amend the
above-identified application as follows: